

# Moment Balancing Templates: Constructions to Add Insertion/Deletion Correction Capability to Error Correcting or Constrained Codes

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**Abstract**—Templates are constructed to extend arbitrary additive error correcting or constrained codes, i.e., additional redundant bits are added in selected positions to balance the moment of the codeword. The original codes may have error correcting capabilities or constrained output symbols as predetermined by the usual communication system considerations, which are retained after extending the code. Using some number theoretic constructions in the literature, insertion/deletion correction can then be achieved. If the template is carefully designed, the number of additional redundant bits for the insertion/deletion correction can be kept small—in some cases of the same order as the number of parity bits in a Hamming code of comparable length.

**Index Terms**—Constrained code, extended codes, insertions/deletions, moment function, number theoretic codes.

## I. INTRODUCTION

THE topic of correcting bit or symbol insertions/deletions in coded sequences has been studied since the 1960s, see, e.g., [1]–[13]. However, it remains a difficult field with relatively few published results. Some recent results on relevant coding techniques may be found in, for example, [9]–[11].

This work deals with deterministic code constructions, as introduced by Levenshtein [6]. These constructions guarantee some insertion/deletion correction capability within a fixed length codeword. In contrast, the coding techniques presented in [12] and [13] correct multiple insertions/deletions, but only in a probabilistic sense.

One drawback of the deterministic code constructions in [3]–[6] is that the codeword boundaries must be known. For this purpose, periodic markers or synchronization sequences can be inserted between codewords. In fact, many practical communication systems do insert these after a long sequence containing many short codewords. However, if a synchronization word is to be inserted after every single transmitted short codeword, the amount of additional redundancy or overhead may become unattractive.

Manuscript received June 23, 2008; revised February 20, 2009. Current version published July 15, 2009. This paper was presented in part at the IEEE International Symposium on Information Theory, Nice, France, June 2007 and in part at the IEEE Information Theory Workshop, Porto, Portugal, May 2008.

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Communicated by H.-A. Loeliger, Associate Editor for Coding Techniques. Digital Object Identifier 10.1109/TIT.2009.2023682

Lately, however there has been much interest in codes with very long codewords and superior additive error correction. Note that, in this context, we sometimes also refer to an additive error as a “reversal” or “substitution” error. These long codes include the important class of low-density parity-check (LDPC) codes. For such long codes, marking the boundaries by inserting a short synchronization sequence after each codeword may be tolerable in terms of overhead.

Note also that the powerful long LDPC codes can operate at very low signal-to-noise ratios, still with significant additive error correction gain—a noise environment where bit synchronization failures, leading to insertions/deletions, may indeed occur more frequently. We see this work as a step towards codes that will work in such extreme conditions. Another application for insertion/deletion codes is in magnetic recoding systems that use bit patterned media, where the possibility of write synchronization errors exist [14].

Data storage systems such as magnetic or optical recording systems furthermore have channel input constraints which can usually be modeled as either  $(d, k)$  constraints or as spectral constraints.

In most communication or data storage systems, additive errors dominate. For example, on some magnetic recording channels, errors which can be modeled as reversal errors can occur with probability  $10^{-6}$ , while errors which can be modeled as insertions or deletions occur with probability a few orders of magnitude lower. However, if insertions/deletions do occur, the consequences can be catastrophic, with large blocks or files of data being lost. In view of the above mentioned error probabilities, it seems appropriate to concentrate most of the error correcting code redundancy to correct reversal errors, but also to make limited provision for insertion/deletion correction.

In this paper, we thus develop coding systems in which the inner code on the channel may be firstly specified to comply with the communication system’s additive error correction or constrained symbol requirements. Next, the inner channel code is extended by adding some additional parity or redundant bits, based on moment balancing templates, to create the option of correcting insertions/deletions, if such errors are detected by means of short synchronization sequences inserted between long codewords. Our constructions can be applied to error correcting codes as well as constrained codes serving as inner channel codes.

The moment balancing templates in this paper were inspired by the constructions in [15] which were developed for the systematic encoding of Levenshtein and Constantin-Rao codes.

The new templates in this paper can be used for the systematic encoding of different classes of number theoretic codes in the literature such as in [3]–[6]. Other work involving systematic encoding of insertion/deletion correcting codes appears in [3], [4], and [16]. Especially when applied to constrained codes or when extending linear error correcting codes, the terminology “moment balancing” may be a more explicit description than “systematic encoding.”

The rest of this paper is organized as follows. In Section II we present the concept of universal moment balancing templates, illustrated with a simple numerical example. In Section III we present templates for Levenshtein’s two classes of insertion/deletion correcting codes and in Section IV for Tenengolts’ binary insertion/deletion correcting codes. We next consider templates for input constrained channels, namely dc-free templates in Section V and  $(d, k)$ -constrained templates in Section VI. The results and possible future work are discussed in Section VII.

## II. MOMENT BALANCING TEMPLATES

Most binary codes correcting insertion/deletion errors impose a condition of the form  $\sum_{i=1}^n v_i x_i \equiv a \pmod{m}$  for some integers  $v_i, a$  and  $m$ , where  $v_i$  is the weight assigned to index  $i$  and  $(x_1 x_2 \dots x_n)$  is a codeword. Therefore, it is convenient to define the moment of a binary sequence as follows.

*Definition 1:* Let  $\mathbf{x} = (x_1 x_2 \dots x_n)$  be a binary sequence of length  $n$ . The (first-order) moment of  $\mathbf{x}$  is defined as

$$\sum_{i=1}^n v_i x_i \tag{1}$$

where  $v_i$  is the weight assigned to index  $i$ .

Typically  $v_i = i$  or  $v_i = i - 1$ . Therefore, we define  $\sigma(\mathbf{x}) = \sum_{i=1}^n i x_i$  and  $\sigma'(\mathbf{x}) = \sum_{i=1}^n (i - 1) x_i$ . In the following, we develop the case  $v_i = i$  as the other case can be similarly developed.

*Definition 2:* Let  $C$  be a binary code of length  $n$  and  $N_C(i)$  be the number of codewords  $\mathbf{x}$  such that  $\sigma(\mathbf{x}) = i$ . Let

$$S_C = \{i : N_C(i) > 0\} \tag{2}$$

be the set of moments of  $C$ . The moment spectrum of the code  $C$  can be represented by the polynomial

$$M_C(y) = \sum_{i \in S_C} N_C(i) y^i. \tag{3}$$

Let  $C$  be a  $(K, k)$  binary code, i.e., a binary code, which is not necessarily linear, of length  $K$  that has  $2^k$  codewords. We are interested in mapping each codeword  $\mathbf{c} = (c_1 c_2 \dots c_K)$  in the code  $C$  into a distinct sequence  $\mathbf{x} = (x_1 x_2 \dots x_n)$  whose moment is congruent to some fixed integer modulo some number. This is possible if and only if  $2^k$  is at most equal to the number of sequences of length  $n$  satisfying this condition. However, our work is motivated by practical considerations which require both the map and its inverse to be easily implementable. For this reason, we focus on maps in which the code bits  $c_1 c_2 \dots c_K$  appear in fixed positions in the sequence  $\mathbf{x}$ , i.e.,  $c_i = x_{\gamma(i)}$

TABLE I  
CODEBOOK AND MOMENTS FOR LDPC (7,3) CODE

$i =$	1	2	3	4	5	6	7	$\sigma$
0	0	0	0	0	0	0	0	0
0	0	1	0	1	1	1	1	21
0	1	0	1	1	1	0	0	17
0	1	1	1	0	0	1	1	16
1	0	0	1	0	1	1	1	18
1	0	1	1	1	0	0	0	13
1	1	0	0	1	0	1	1	15
1	1	1	0	0	1	0	0	12

for some  $1 \leq \gamma(1) < \gamma(2) < \dots < \gamma(K) \leq n$ . The remaining bits in  $\mathbf{x}$ , which are called balancing bits and denoted by  $b_1, b_2, \dots, b_{n-K}$ , are positioned in the  $n - K$  positions that are not occupied by code bits. In particular,  $b_i = x_{\beta(i)}$  where  $1 \leq \beta(1) < \beta(2) < \dots < \beta(n - K) \leq n$  and  $\cup_i \gamma(i)$  and  $\cup_i \beta(i)$  are disjoint sets whose union is  $\{1, 2, \dots, n\}$ . Let  $\sigma_c(\mathbf{x}) = \sum_{i=1}^K \gamma(i) c_i$  and  $\sigma_b(\mathbf{x}) = \sum_{i=1}^{n-K} \beta(i) b_i$ . Then,  $\sigma_c(\mathbf{x})$  and  $\sigma_b(\mathbf{x})$ , which we also denote by  $\sigma_c$  and  $\sigma_b$  if  $\mathbf{x}$  is not specified, indicate the contribution of the code bits and the balancing bits, respectively, to the moment of  $\mathbf{x}$ , defined in (1). In particular,  $\sigma_c(\mathbf{x}) + \sigma_b(\mathbf{x}) = \sigma(\mathbf{x})$ .

*Definition 3:* We define a *universal* moment balancing template as a sequence of code bit positions and balancing bit positions such that  $\sigma_b$  can assume any integer value modulo  $m$  for some fixed positive integer  $m$ .

In Section III we shall elaborate on the role of  $m$ .

In particular, the moment balancing template is specified by the number and the positions of the balancing bits. For each sequence of code bits, the balancing bits are chosen such that  $\sigma_c + \sigma_b$  assumes a specific value modulo  $m$ . The collection of all sequences of code bits extended with balancing bits forms an  $(n, k)$  code  $C_{\text{Ext}}$ . Since the balancing bits can be chosen so that their contribution to the moment assumes any given integer value modulo  $m$ , the number of balancing bits,  $n - K$ , in a universal moment balancing template is lower bounded by

$$n - K \geq \lceil \log_2 m \rceil. \tag{4}$$

*Example 1:* Consider the  $(n, k) = (7, 3)$  LDPC code  $C$  from [17, p. 858]. In Table I, we list all codewords, and their corresponding moments,  $\sigma$ . Note, from (2) and (3), that  $S_C = \{0, 12, 13, 15, 16, 17, 18, 21\}$  and

$$M_C(y) = 1 + y^{12} + y^{13} + y^{15} + y^{16} + y^{17} + y^{18} + y^{21}.$$

In Table II, we extend the code to an  $(11, 3)$  code,  $C_{\text{Ext}}$ , by using a universal moment balancing template with four balancing bits  $b_i$  at positions 1, 2, 4, and 8 and in the extended code set  $x_1 = b_1^1, x_2 = b_2^2, x_4 = b_3^4$  and  $x_8 = b_4^8$ . The code bits  $c_1^3, c_2^5, \dots, c_7^{11}$  from Table I are written into the remaining positions. The superscripts are added for the reader’s benefit to indicate the position of each bit in the sequence. Note that we have chosen the balancing bits in order to have  $S_{C_{\text{Ext}}} = \{0, 36\}$  and

$$M_{C_{\text{Ext}}}(y) = 1 + 7y^{36}.$$

TABLE II  
LDPC (7,3) CODE WITH BALANCING BITS AND MOMENTS

$b_1^1$	$b_2^2$	$c_1^3$	$b_3^4$	$c_2^5$	$c_3^6$	$c_4^7$	$b_4^8$	$c_5^9$	$c_6^{10}$	$c_7^{11}$	$\sigma_c$	$\sigma_b$	$\sigma$
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	1	1	1	36	0	36
1	0	0	1	1	0	1	0	1	1	0	31	5	36
1	1	0	1	1	1	1	0	0	0	1	29	7	36
1	0	1	1	0	0	1	0	0	1	1	31	5	36
1	1	1	0	0	1	1	1	1	0	0	25	11	36
0	0	1	0	1	0	0	1	1	0	1	28	8	36
0	0	1	1	1	1	0	1	0	1	0	24	12	36

In Sections III–VI we develop moment balancing templates in order to extend arbitrary linear additive error correcting codes or constrained codes, to endow them with the option of insertion/deletion correction.

### III. TEMPLATES FOR LEVENSHTEIN’S INSERTION/DELETION CORRECTING CODES

Levenshtein presented two classes of single insertion/deletion correcting codes in [6]. A code of length  $n$  in each class consists of binary sequences  $\mathbf{x} = (x_1 x_2 \dots x_n)$  satisfying  $\sigma(\mathbf{x}) = \sum_{i=1}^n i x_i \equiv a \pmod{m}$  where  $a$  and  $m$  are fixed integers.

#### A. Levenshtein’s First Class of Insertion/Deletion Correcting Codes

In this class  $m \geq n + 1$  and the code can correct a single insertion or deletion error. The code’s cardinality can be maximized by setting  $m = n + 1$  and  $a = 0$ , which we will assume in the following.

As a universal moment balancing template, we take  $\beta(i) = 2^{i-1}$  for  $i = 1, 2, \dots, \lceil \log_2(n + 1) \rceil$ . Clearly,

$$\sum_{i=1}^{\lceil \log_2(n+1) \rceil} \beta(i) b_i = \sum_{i=1}^{\lceil \log_2(n+1) \rceil} 2^{i-1} b_i$$

assumes any given value in  $\{0, 1, \dots, n\}$  for some choice of the  $b_i$ ’s. Notice, from (4), that the number of balancing bits,  $\lceil \log_2(n + 1) \rceil$ , is minimal for a universal moment balancing template with parameter  $m = n + 1$ . The moment balancing template is represented symbolically by the sequence

$$b_1^1 b_2^2 c_1^3 b_3^4 c_2^5 c_3^6 c_4^7 b_4^8 c_5^9 \dots b_{\lceil \log_2(n+1) \rceil}^{2^{\lceil \log_2(n+1) \rceil - 1}} c_{2^{\lceil \log_2(n+1) \rceil - 1} + 1}^{2^{\lceil \log_2(n+1) \rceil - 1} + 1} \dots c_K^n$$

This template can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  that can correct a single insertion/deletion from any  $(K, k)$  code  $C$ , where  $K = n - \lceil \log_2(n + 1) \rceil$ . The rate of  $C_{\text{Ext}}$ ,  $k/n$ , approaches the rate,  $k/K$ , of the code  $C$  as its length,  $K$ , increases.

Note that for the  $n = 11$  code in Table II,  $\sigma(\mathbf{x}) \equiv 0 \pmod{12}$  hence we have extended the  $(7, 3)$  code to become a single insertion/deletion correcting  $(11, 3)$  code. It can also be considered as a subcode of the  $n = 11$  Levenshtein code, or following [15], the  $(11, 3)$  code is obtained by systematic encoding of the  $n = 11$  Levenshtein code, where we wrote the code bits of the  $(7, 3)$  code into the information bit positions of the Levenshtein code.

TABLE III  
REDUNDANCY RATES OF THE EXTENDED FIRST-CLASS LEVENSHTEIN CODE

WITH $m = n + 1$ AND $a = 0$		
Redundancy Rate	$K$	$n$
44.44%	5	9
28.57%	10	14
10.71%	50	56
6.54%	100	107
1.77%	500	509
0.99%	1000	1010

Table III represents the redundancy rates of Levenshtein’s first class of insertion/deletion correcting codes constructed by using the moment balancing template. This indicates a fast redundancy rate decrease with increasing information length.

#### B. Levenshtein’s Second Class of Insertion/Deletion Correcting Codes

In this class  $m \geq 2n$  and the code can correct either a single insertion/deletion error or a single reversal error. The code’s cardinality can be maximized by setting  $m = 2n$  and  $a = 0$ . By appending one additional balancing bit, we can set up a corresponding moment balancing template. As a universal moment balancing template, we take  $\beta(i) = 2^{i-1}$  for  $i = 1, 2, \dots, \lceil \log_2 n \rceil$  and  $\beta(\lceil \log_2 n \rceil + 1) = n$ . Indeed

$$\sum_{i=1}^{\lceil \log_2 n \rceil + 1} \beta(i) b_i = \sum_{i=1}^{\lceil \log_2 n \rceil} 2^{i-1} b_i + n b_{\lceil \log_2 n \rceil + 1}$$

assumes any given value in  $\{0, 1, \dots, 2n - 1\}$  for some choice of the  $b_i$ ’s. Notice, from (4), that the number of balancing bits,  $\lceil \log_2 n \rceil + 1$ , is minimal for a universal moment balancing template with parameter  $m = 2n$ . The moment balancing template is represented symbolically by the sequence

$$b_1^1 b_2^2 c_1^3 b_3^4 c_2^5 c_3^6 c_4^7 b_4^8 c_5^9 \dots b_{\lceil \log_2 n \rceil}^{2^{\lceil \log_2 n \rceil - 1}} c_{2^{\lceil \log_2 n \rceil - 1} + 1}^{2^{\lceil \log_2 n \rceil - 1} + 1} \dots c_K^{n-1} b_{\lceil \log_2 n \rceil + 1}^n$$

This template can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  that can correct either a single insertion/deletion or a single reversal error from a  $(K, k)$  code  $C$ , where  $K = n - \lceil \log_2 n \rceil - 1$ . The rate of  $C_{\text{Ext}}$ ,  $k/n$ , approaches the rate,  $k/K$ , of the code  $C$  as its length  $K$  increases.

### IV. TEMPLATES FOR TENENGGOLTS’ BINARY INSERTION/DELETION CORRECTING CODES

This class of codes, published in [3] can correct a single deletion and an error in the preceding bit. Here  $\mathbf{x}$  is a codeword if

$$\sigma'(\mathbf{x}) = \sum_{i=1}^n (i - 1) x_i \equiv a \pmod{2n - 2} \tag{5}$$

and

$$\sum_{i=1}^n x_i \equiv b \pmod{2} \tag{6}$$

where  $a$  and  $b$  are fixed integers. As a universal moment balancing template, we take  $\beta(1) = 1$ ,  $\beta(i) = 2^{i-2} + 1$  for

$i = 2, 3, \dots, \lceil \log_2(n-1) \rceil + 1$ , and  $\beta(\lceil \log_2(n-1) \rceil + 2) = n$ .  
Indeed

$$\sum_{i=1}^{\lceil \log_2(n-1) \rceil + 2} (\beta(i) - 1)b_i = \sum_{i=2}^{\lceil \log_2(n-1) \rceil + 1} 2^{i-2}b_i + (n-1)b_{\lceil \log_2(n-1) \rceil + 2}$$

assumes any given value in  $\{0, 1, \dots, 2n-3\}$  for some choice of the  $b_i$ 's for  $i = 2, 3, \dots, \lceil \log_2(n-1) \rceil + 2$  to satisfy (5), while  $b_1$  can be independently specified to satisfy (6). The number of balancing bits in this template is  $\lceil \log_2(n-1) \rceil + 2$ .

The moment balancing template is represented symbolically by the sequence

$$b_1^1 b_2^2 b_3^3 c_1^4 b_4^5 c_2^6 c_3^7 c_4^8 b_5^9 \dots b_{\lceil \log_2(n-1) \rceil - 1}^{2^{\lceil \log_2(n-1) \rceil - 1} + 1} \dots c_{2^{\lceil \log_2(n-1) \rceil - 1} - \lceil \log_2(n-1) \rceil + 1}^{2^{\lceil \log_2(n-1) \rceil - 1} + 2} \dots c_K^{n-1} b_{\lceil \log_2(n-1) \rceil + 2}^n$$

This template can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  that can correct a single deletion and an error in the preceding bit from a  $(K, k)$  code  $C$ , where  $K = n - \lceil \log_2(n-1) \rceil - 2$ . The rate of  $C_{\text{Ext}}$ ,  $n/k$ , approaches the rate,  $k/K$ , of the code  $C$  as its length,  $K$ , increases.

We should note that Tenengolts in [3] presented a systematic version of his code that can be used to construct an  $(n, k)$  code from the  $(K, k)$  code where  $n = K + \lceil \log_2(K-1) \rceil + 6$ . For example, with  $K = 6$ , this version requires a code of length  $n = 15$ , which includes two bits that act as markers between codewords, while our code  $C_{\text{Ext}}$  has length  $n = 12$ .

### V. TEMPLATES FOR DC-FREE CODES

In this section, we consider a class of constrained codes, namely, dc-free codes. A dc-free code is characterized by the fact that it has zero power at zero frequency [18]. We consider the class of codes that achieve this property by virtue of being weight balanced, i.e., each codeword contains an equal number of 0's and 1's. To preserve this property, a new approach by adding a fixed number of paired bits at determinate indices of the codeword is used to balance the moment of the codeword.

Let  $c_1 c_2 \dots c_K$  be the original dc-free codeword, where  $c_i \in \{0, 1\}$ , satisfies

$$\sum_{i=1}^K c_i = \frac{K}{2}$$

and  $K$  is a positive even integer. We propose a pairwise template to moment balance this dc-free codeword. In other words, we insert a fixed number of paired moment balancing bits in the sequence. It is evident that, if each pair of the moment balancing bits appears in the complementary form of "01" or "10", the final codeword is still dc-free. The length of the balanced codeword  $n$ , after the insertion of the balancing bits, is assumed to be an even number such that  $n - 2^{\lceil \log_2 n \rceil} \neq 0$  or 2, i.e.,  $n$  is neither a power of two, nor two more than a power of two. We will design a template of length  $n$  with  $t = \lceil \log_2(n+1) \rceil$  pairs of moment balancing bits that supports the encoding of dc-free code-

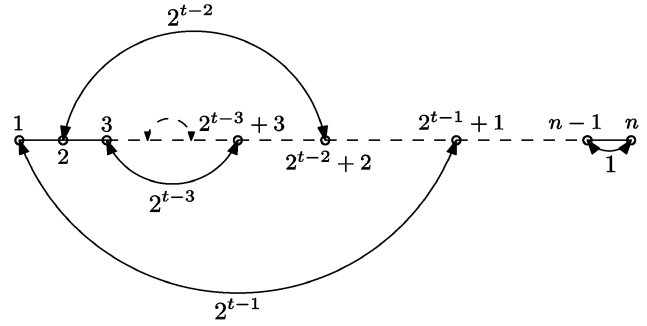


Fig. 1. Indices of the pairwise moment balancing bits in a moment balanced dc-free codeword.

words from a code  $C$  of length  $K = n - 2t = n - 2\lceil \log_2(n+1) \rceil$  into a code  $C_{\text{Ext}}$  that can correct a single insertion/deletion.

We set the balancing bits in the template according to

$$\beta(i) = \begin{cases} i, & \text{for } 1 \leq i \leq t-1 \\ 2^{i-t+1} + 2t - i - 1, & \text{for } t \leq i \leq 2t-2 \\ n-1, & \text{for } i = 2t-1 \\ n, & \text{for } i = 2t. \end{cases}$$

Symbolically the template can be represented as follows:

$$b_1^1 b_2^2 \dots b_i^i \dots b_{t-1}^{t-1} c_1^t b_{t+1}^{t+1} b_{t+2}^{t+2} c_2^{t+3} c_3^{t+4} b_{t+5}^{t+5} c_4^{t+6} c_5^{t+7} \dots b_i^{2^{i-t+1} + 2t - i - 1} \dots b_{2t-2}^{2^{t-1} + 1} c_{2^{t-1} - 2t + 4}^{2^{t-1} + 2} \dots c_K^{n-2} b_{2t-1}^{n-1} b_{2t}^n \quad (7)$$

The template can also be illustrated by Fig. 1, where it can be seen which balancing bits are paired. The values on the arcs indicate the difference between the index values for the paired bits.

To simplify the notation and make the  $\beta$ 's pair up, let

$$b'_i = b_{2t-i-1} \text{ and } \beta'(i) = \beta(2t-i-1) \quad (8)$$

for  $1 \leq i \leq t-1$ . Then the bit in position  $\beta(i)$  is paired with the bit in position  $\beta'(i)$  and to have complementary bits, we set

$$b_i = \overline{b'_i}. \quad (9)$$

In templates from previous sections, only one bit (either a 1 or a 0) was put in a balancing position, where the contribution,  $\beta(i)b_i$ , to  $\sigma_b$  was either  $\beta(i)$  or 0. The difference in  $\sigma_b$  between the two choices is trivially

$$\beta(i) - 0 = \beta(i).$$

However, in this template a pair of bits (either 01 or 10) must be placed in paired balancing positions, therefore the contribution,  $\beta(i)b_i + \beta'(i)b'_i$ , to  $\sigma_b$  is either  $\beta(i)$  or  $\beta'(i)$ . The difference in  $\sigma_b$  is

$$\beta'(i) - \beta(i) = 2^{t-i} \quad (10)$$

for  $i \in \{1, 2, \dots, t-1\}$ .

**Theorem 1:** Let  $n$  be an even positive integer such that  $n - 2^{\lceil \log_2 n \rceil} \neq 0$  or 2. Then, the sequence (7) is a valid template

that can be used to encode any dc-free sequence  $c_1c_2\dots c_K$  into a dc-free code that can correct a single insertion/deletion by setting  $(b_i, b'_i) = (0, 1)$  or  $(b_i, b'_i) = (1, 0)$  for  $i \in \{1, 2, \dots, t-1\}$  and  $(b_{2t-1}, b_{2t}) = (0, 1)$  or  $(b_{2t-1}, b_{2t}) = (1, 0)$ .

*Proof:* First we show that the template in (7) is valid, i.e., the positions of the balancing bits are well defined. For  $1 \leq i \leq t-1$ ,  $\beta(i) = i$  and  $\beta(i)$  assumes different values in the range  $1 \leq \beta(i) \leq t-1$ . For  $t \leq i \leq 2t-2$ ,  $\beta(i) = 2^{i-t+1} + 2t - i - 1$  and  $\beta(i)$  assumes different values in the range  $t+1 \leq \beta(i) \leq 2^{t-1} + 1$ . We also have  $\beta(2t-1) = n-1$  and  $\beta(2t) = n$ . We can write  $n = 2^s + p$  where  $s = \lfloor \log_2 n \rfloor$  and  $p = n - 2^{\lfloor \log_2 n \rfloor}$ . Since  $n$  is even and  $n - 2^{\lfloor \log_2 n \rfloor} \neq 0$  or  $2$ , then  $p \geq 4$ . Hence,  $t = \lfloor \log_2(n+1) \rfloor = s+1$  and  $2^{t-1} + 1 = 2^s + 1 < 2^s + p - 1 = n - 1 = \beta(2t-1)$ . This proves that all balancing bits occupy distinct positions between 1 and  $n$ . Next, we show that the contribution of the balancing bits to the moment,  $\sigma_b$ , can assume any integer value modulo  $n+1$  by proper choice of  $b'_i$  for  $i = 1, 2, \dots, t-1$  and  $b_{2t}$ . Taking (8) into account, the net contribution from the balancing pairs is

$$\begin{aligned} \sigma_b &= \sum_{i=1}^{2t} \beta(i)b_i \\ &= \sum_{i=1}^{t-1} [\beta(i)b_i + \beta'(i)b'_i] + (n-1)b_{2t-1} + nb_{2t}. \end{aligned} \quad (11)$$

Considering (9) and that  $b_{2t-1} = \bar{b}_{2t}$ , combine (10) with (11) to get

$$\begin{aligned} \sigma_b &= \sum_{i=1}^{t-1} [\beta(i)b_i + (2^{t-i} + \beta(i))b'_i] + (n-1)\bar{b}_{2t} + nb_{2t} \\ &= \sum_{i=1}^{t-1} \beta(i)(\bar{b}'_i + b'_i) + \sum_{i=1}^{t-1} 2^{t-i}b'_i \\ &\quad + (n-1)(\bar{b}_{2t} + b_{2t}) + b_{2t} \\ &= \sum_{i=1}^{t-1} \beta(i) + \sum_{i=1}^{t-1} 2^{t-i}b'_i + n - 1 + b_{2t} \\ &= \Lambda + \sum_{i=1}^{t-1} 2^{t-i}b'_i + \bar{b}_{2t} \end{aligned} \quad (12)$$

where

$$\Lambda = \frac{t(t-1)}{2} + n - 1.$$

It is evident from (12) that the terms added to the constant  $\Lambda$  can enumerate all the values in  $\{0, 1, \dots, 2^t - 1\}$  by judiciously choosing  $b_{2t-2} = b'_1, b_{2t-3} = b'_2, \dots, b_t = b'_{t-1}$  and  $b_{2t}$ . As  $2^t = 2^{\lfloor \log_2(n+1) \rfloor} \geq n+1$ ,  $\sigma_b$  can assume any desired value modulo  $n+1$  by proper choice of the balancing bits.  $\square$

We notice that the last pair  $(b_{2t-1}, b_{2t})$  of moment balancing bits can be placed at any indices that are adjacent but unoccupied by other balancing bits. To simplify notation, we choose to set them in the last two positions.

The template in (7) can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  satisfying the dc-free constraint that can correct a single insertion/deletion from a  $(K, k)$  code  $C$  satisfying the dc-free

constraint, where  $K = n - 2\lfloor \log_2(n+1) \rfloor$  for large  $n$ . The rate of  $C_{\text{Ext}}$ ,  $k/n$ , approaches the rate,  $k/K$ , of the code  $C$  as its length,  $K$ , increases.

## VI. TEMPLATES FOR $(d, k)$ -CONSTRAINED CODES

We next develop templates for  $(d, k)$ -constrained codes, where  $d$  is the minimum number of 0's and  $k$  is the maximum number of 0's between any two 1's in the coded sequences. Codes with  $d = 1$  and  $d = 2$  find the widest application in practical recording systems [18]. Usually the  $d$  restriction should be enforced, while the  $k$  restriction can be somewhat relaxed in the coded sequences.

In the following example, we assume that the code  $C$  is  $(d = 1)$ -constrained. Our objective is to maintain this constraint while giving the code the capability to correct a single insertion/deletion by using sequences in Levenshtein's first class of insertion/deletion correcting code. We use the template defined by  $\beta(i) = i$  for  $i = 1, 2, \dots, 5$  and  $\beta(i) = 2^{i-3}$  for  $i = 6, 7, \dots, \lfloor \log_2(n+1) \rfloor + 2$ . Furthermore, we place one zero before and after each balancing bit if the neighboring bit, to the left and to the right, respectively, is not a balancing bit. The bits  $b_1, \dots, b_5$  can always be chosen to satisfy the  $(d = 1)$ -constraint and contribute the moments  $0, 1, \dots, 7$  as follows:

$$\sigma(b_1 \dots b_5) = \begin{cases} 0, & \text{for } b_1b_2b_3b_4b_5 = 00000 \\ j, & \text{for } b_j = 1 \text{ for } 1 \leq j \leq 5, b_i = 0 \text{ for } i \neq j \\ 6, & \text{for } b_1b_2b_3b_4b_5 = 10001 \\ 7, & \text{for } b_1b_2b_3b_4b_5 = 01001. \end{cases}$$

Hence

$$\sum_{i=1}^{\lfloor \log_2(n+1) \rfloor + 2} \beta(i)b_i = \sigma(b_1b_2 \dots b_5) + \sum_{i=6}^{\lfloor \log_2(n+1) \rfloor + 2} 2^{i-3}b_i$$

assumes any given value in  $\{0, 1, \dots, n\}$  for some choice of the  $b_i$ 's. The moment balancing template is represented symbolically by the sequence

$$b_1^1b_2^2b_3^3b_4^4b_5^500b_6^80c_1^{10}c_2^{11} \dots c_4^{14}0b_7^{16}0 \dots 0b_{\lfloor \log_2(n+1) \rfloor + 2}^{2^{\lfloor \log_2(n+1) \rfloor - 1}} \\ c_{2^{\lfloor \log_2(n+1) \rfloor - 1} + 2}^{2^{\lfloor \log_2(n+1) \rfloor - 1} + 2} c_{2^{\lfloor \log_2(n+1) \rfloor - 1} - 3}^{2^{\lfloor \log_2(n+1) \rfloor + 5}} \dots c_K^n.$$

This template can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  satisfying the  $(d = 1)$ -constraint that can correct a single insertion/deletion from a  $(K, k)$  code  $C$  satisfying the  $(d = 1)$ -constraint, where  $K = n - 3\lfloor \log_2(n+1) \rfloor + 3$  for large  $n$ . The rate of  $C_{\text{Ext}}$ ,  $k/n$ , approaches the rate,  $k/K$ , of the code  $C$  as its length,  $K$ , increases.

A similar construction can be further devised to satisfy the  $(d = 2)$ -constraint using the template

$$b_1^1b_2^2b_3^3b_4^4b_5^500b_6^80c_1^{11}c_2^{12}c_4^{13}00b_7^{16}00 \dots 00b_{\lfloor \log_2(n+1) \rfloor - 1}^{2^{\lfloor \log_2(n+1) \rfloor - 1}} \\ c_{2^{\lfloor \log_2(n+1) \rfloor - 1} + 3}^{2^{\lfloor \log_2(n+1) \rfloor - 1} + 3} c_{2^{\lfloor \log_2(n+1) \rfloor - 1} - 5}^{2^{\lfloor \log_2(n+1) \rfloor + 13}} \dots c_K^n$$

where two zeros are placed before and after each balancing bit if the neighboring bit, to the left and to the right, respectively, is not a balancing bit. The bits  $b_1, \dots, b_5$  and  $b_i$ 's are chosen as before. This template can be used to construct an  $(n, k)$  code  $C_{\text{Ext}}$  satisfying the  $(d = 2)$ -constraint that can correct a single

insertion/deletion from a  $(K, k)$  code  $C$  satisfying the  $(d = 2)$ -constraint, where  $K = n - 5\lceil \log_2(n+1) \rceil + 10$  for large  $n$ . The rate of  $C_{\text{Ext}}$ ,  $k/n$ , approaches the rate,  $k/K$ , of the code  $C$  as its length,  $K$ , increases.

## VII. DISCUSSION

Generalizing and extending our previous work on the systematic encoding of some number theoretic codes, we have developed several universal moment balancing templates. We have shown that these templates can be applied to unconstrained error correcting codes, or to constrained channel codes. The small redundancy added by our moment balancing template may make longer codes attractive for some practical applications.

Note that the moment balancing templates can also be used with convolutional codes. Long coded sequences can be generated and the shift register flushed with 0's. By applying bidirectional Viterbi decoding [13] up to the index with an insertion/deletion, some reversal error correction may be possible.

In this work, we considered *universal* moment balancing templates. These templates guarantee that every sequence of a given length  $K$  can be encoded into a codeword  $\mathbf{x}$  in  $C_{\text{Ext}}$  by inserting a number of balancing bits in given positions such that  $\sigma(\mathbf{x})$  can be made to assume any integer value modulo some positive number  $m$  regardless of the value of  $\sigma_c$ . We can also consider *optimized* moment balancing templates, for a given code  $C$  of length  $K$  and a given integer  $a$ , that guarantees that every sequence from the code  $C$  can be encoded into a codeword  $\mathbf{x}$  in  $C_{\text{Ext}}$  by inserting a number of balancing bits in given positions such that  $\sigma(\mathbf{x}) \equiv a \pmod{m}$ . Since the values assumed by  $\sigma_c$  may be restricted depending on the code  $C$ , and since  $a$  is given, it is not necessary, in an optimized moment balancing template to have  $\sigma_b$  assuming every integer value modulo  $m$ . In particular, the number of balancing bits  $n - K$  in an optimized moment balancing template may be less than the lower bound (4). Therefore, it is possible that the number of balancing bits in an optimized moment balancing template is less than the number of balancing bits in a universal moment balancing template and using optimized templates may lead to higher code rates. Constructing optimized moment balancing templates, for specific codes or classes of codes, is an interesting topic for future research.

Although we were primarily interested in applying the moment balancing templates for insertion/deletion correction, these templates may also be used for the systematic encoding of other codes constructed using the moment function. Future applications may include codes correcting asymmetrical errors, constant weight codes, and spectral shaping codes.

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